Publications

- Books and Book Chapters

- Journals


- Conferences
  1. Z. Wan, X. Xu, W. Rhee, and Z. Wang, “A 0.0048mm² 0.43-to-1.0V 0.54-to-1.76GHz bias-current-free PLL in 14nm FinFET CMOS,” in Proc. IEEE Integrated Circuits, Technologies and Applications (ICTA), Nov. 2021, pp. 1-2.


- Invited Talks in International Conferences & Workshops

**IEEE DL Talks & Webinar**


- **Patents**
  24 U.S. patents issued.

- **Ph.D. Thesis**